[54] ELECTRONIC VIDEO GAME

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[57] ABSTRACT

An electronic game in which a standard television receiver may be a terminal is fed by an interface device which is in turn interconnected to first and second controllers, the first controller interacts with a matrix or display memory, which may be a read-only-memory (ROM) which is controlled by a matrix selector register. The first controller also has a matrix address register which feeds it. The second controller is fed by a dot address register. The matrix memory stores the digital data to control the matrix image desired to be displayed. The controller contains X and Y display address counters which indicate the position of the video beam on a grid at any given time. When the addresses of the beam coincide with the addresses of the positions stored in the memory as determined by a comparator the matrix will be displayed. A similar structure is utilized for a dot display (normally smaller) matrix in which the X and Y display address registers have a dual function working for both dot display and matrix display positions of the game and use a second comparator to control the output of the dot display.

8 Claims, 18 Drawing Figures

